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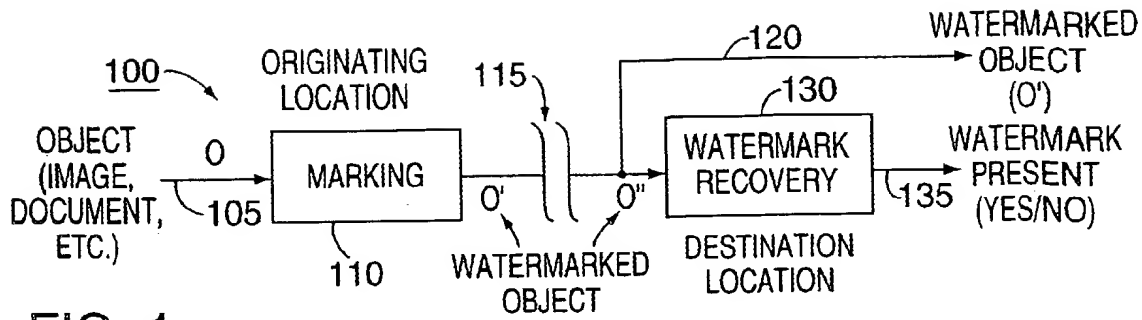


FIG. 1

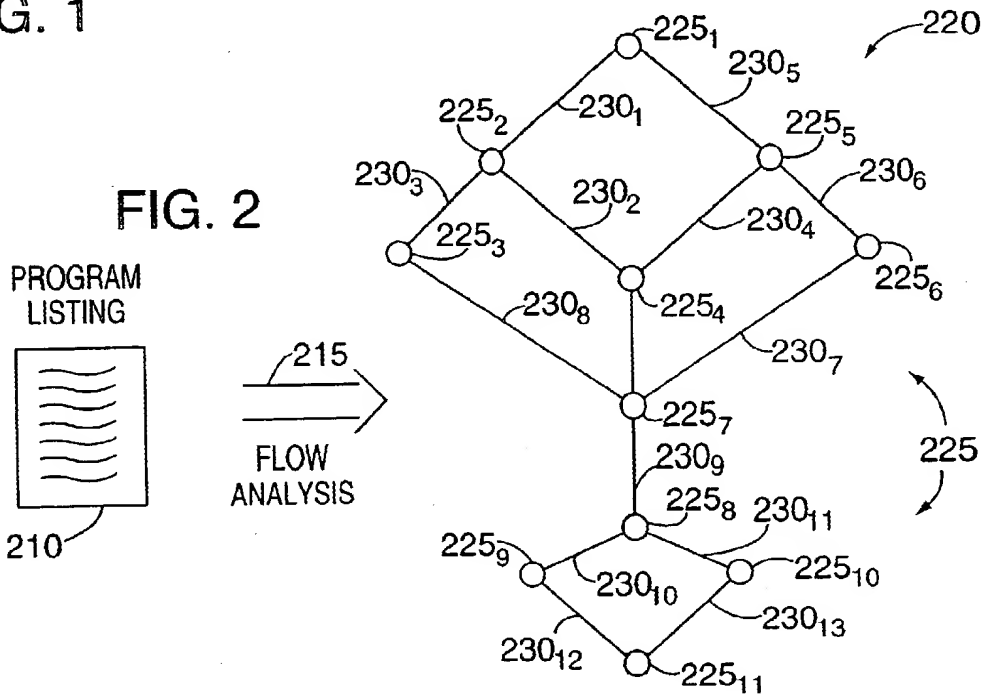


FIG. 2

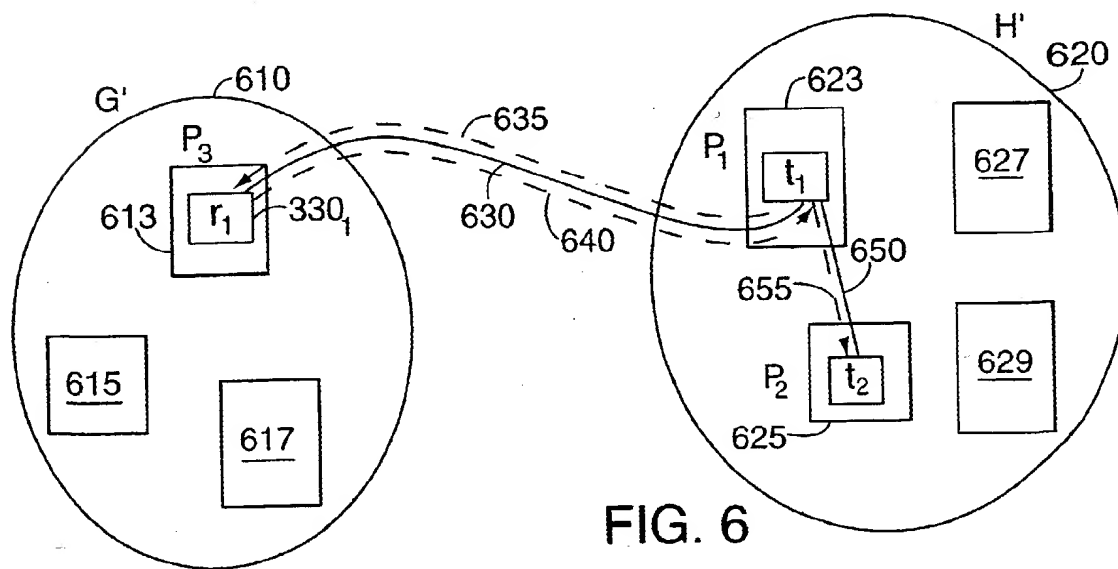
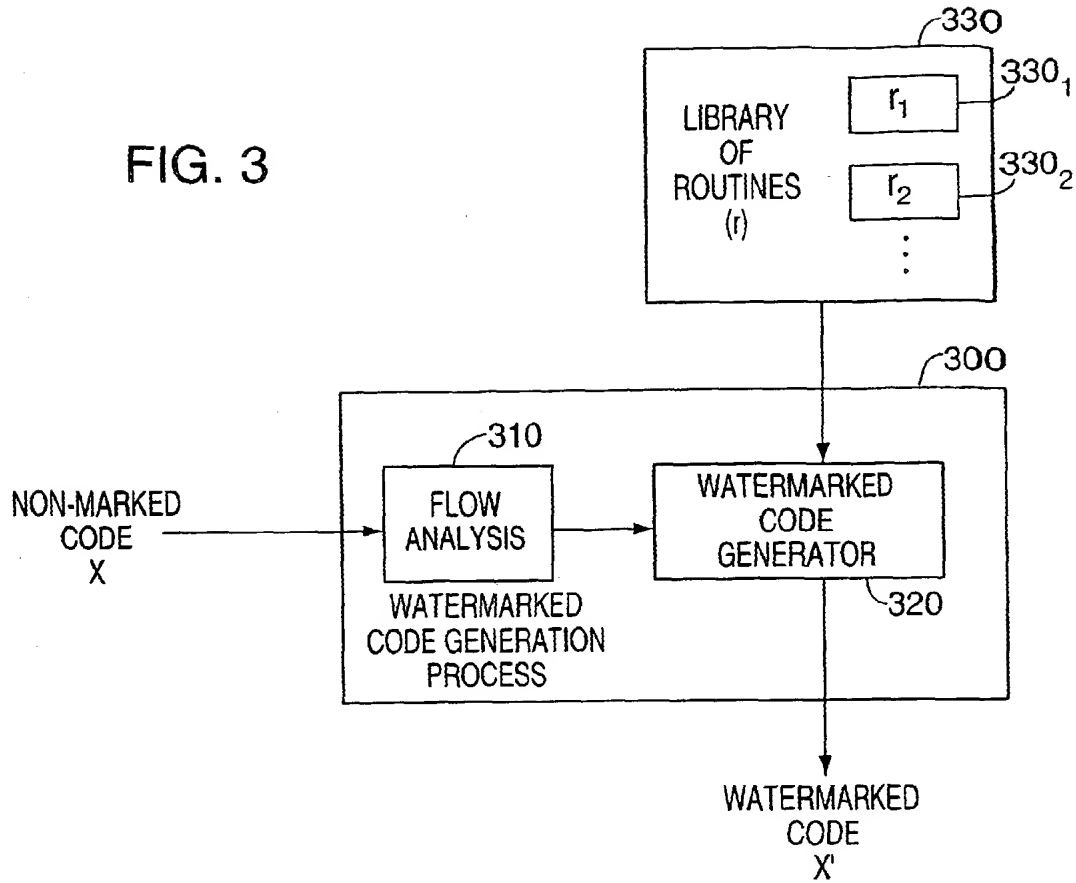


FIG. 6

FIG. 3



INPUTS

	G	H
<u>INPUT CODE PORTIONS</u>	COMPLETE CODE IMAGE	COMPLETE CODE IMAGE
	1/2 CODE IMAGE	1/2 CODE IMAGE
	COMPLETE CODE IMAGE	SECURITY CODE
	SECURITY CODE	COMPLETE CODE IMAGE

500

FIG. 5

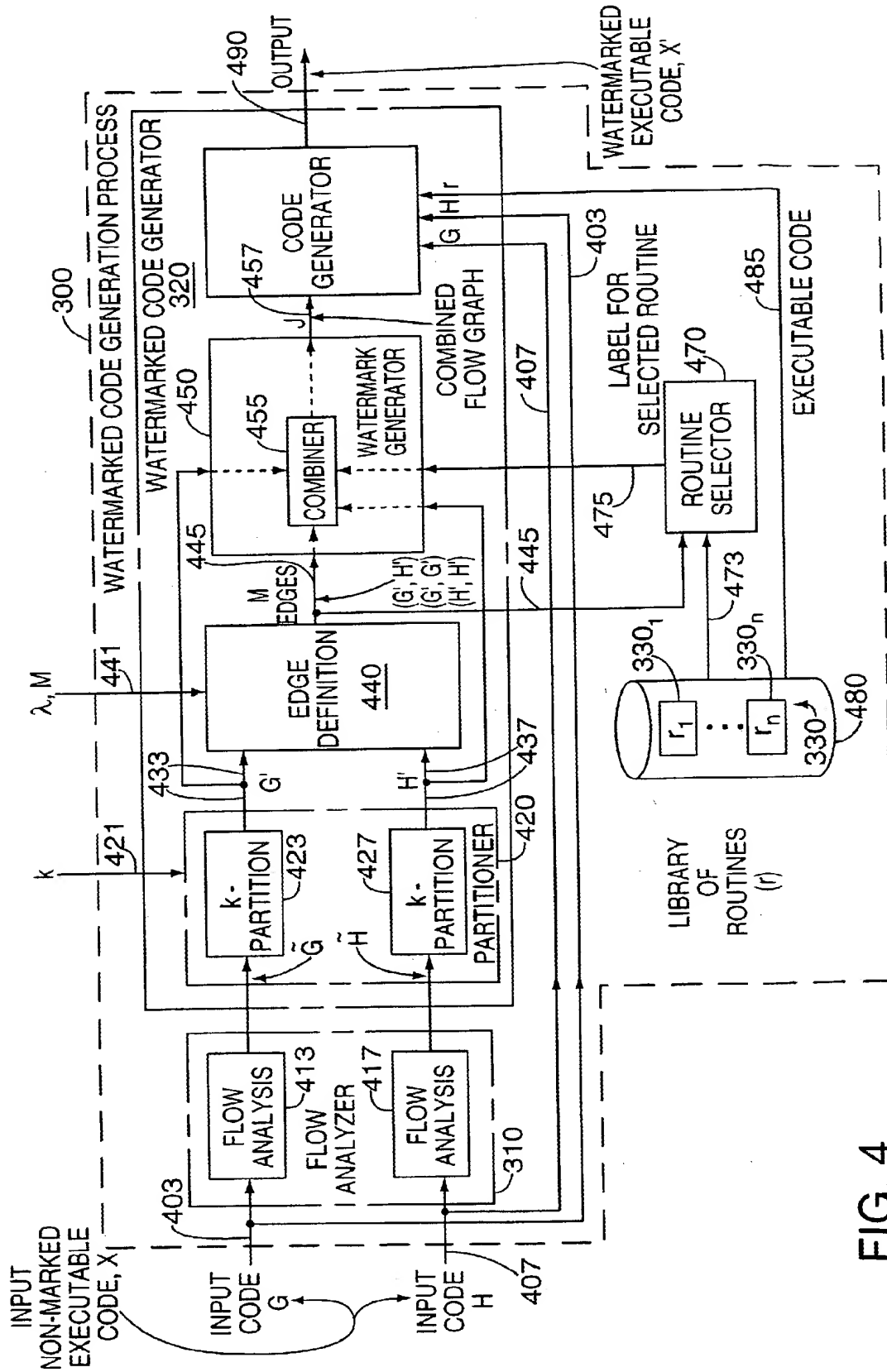


FIG. 4

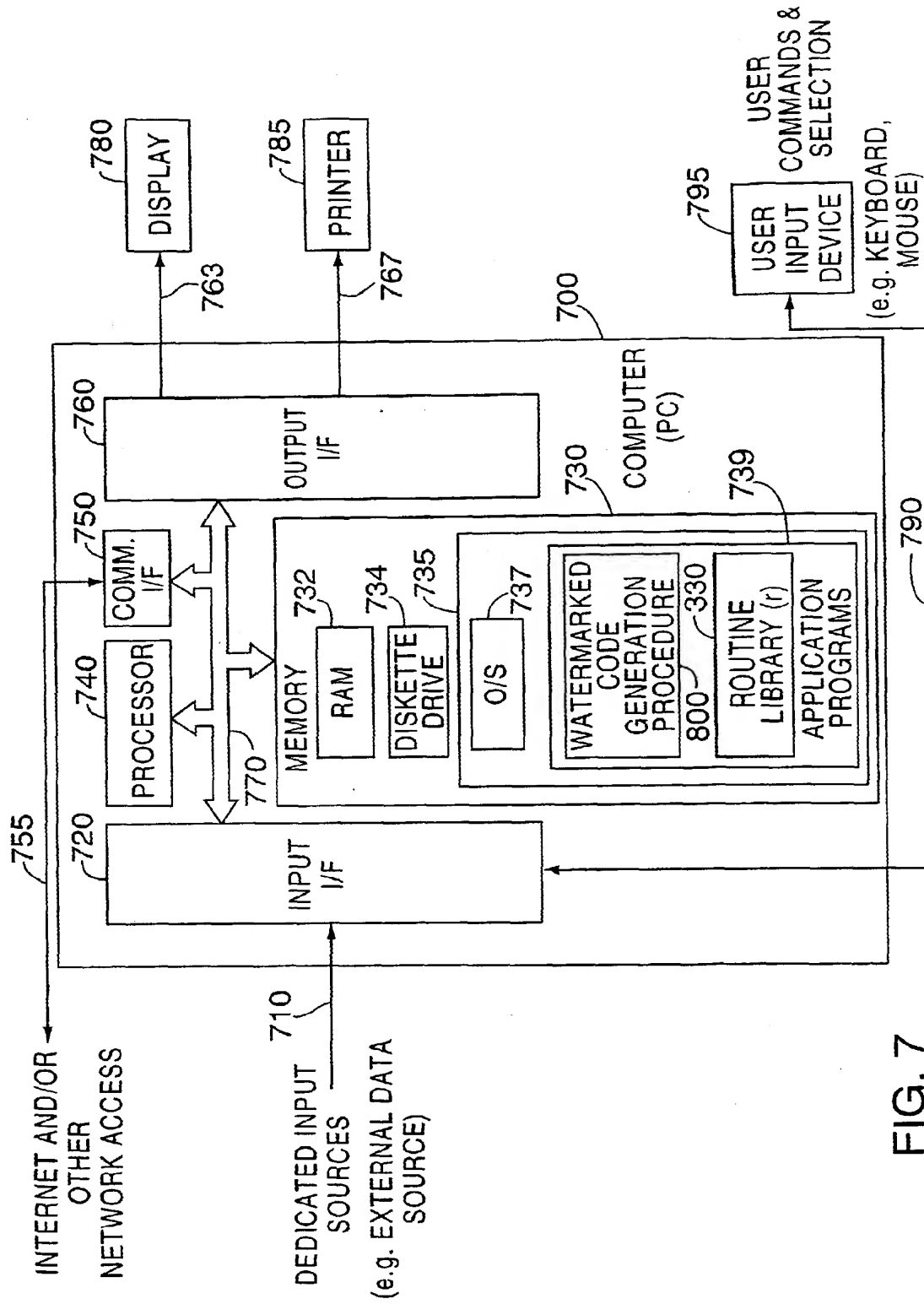


FIG. 7

FIG. 8

WATERMARKED
CODE
GENERATION
PROCEDURE

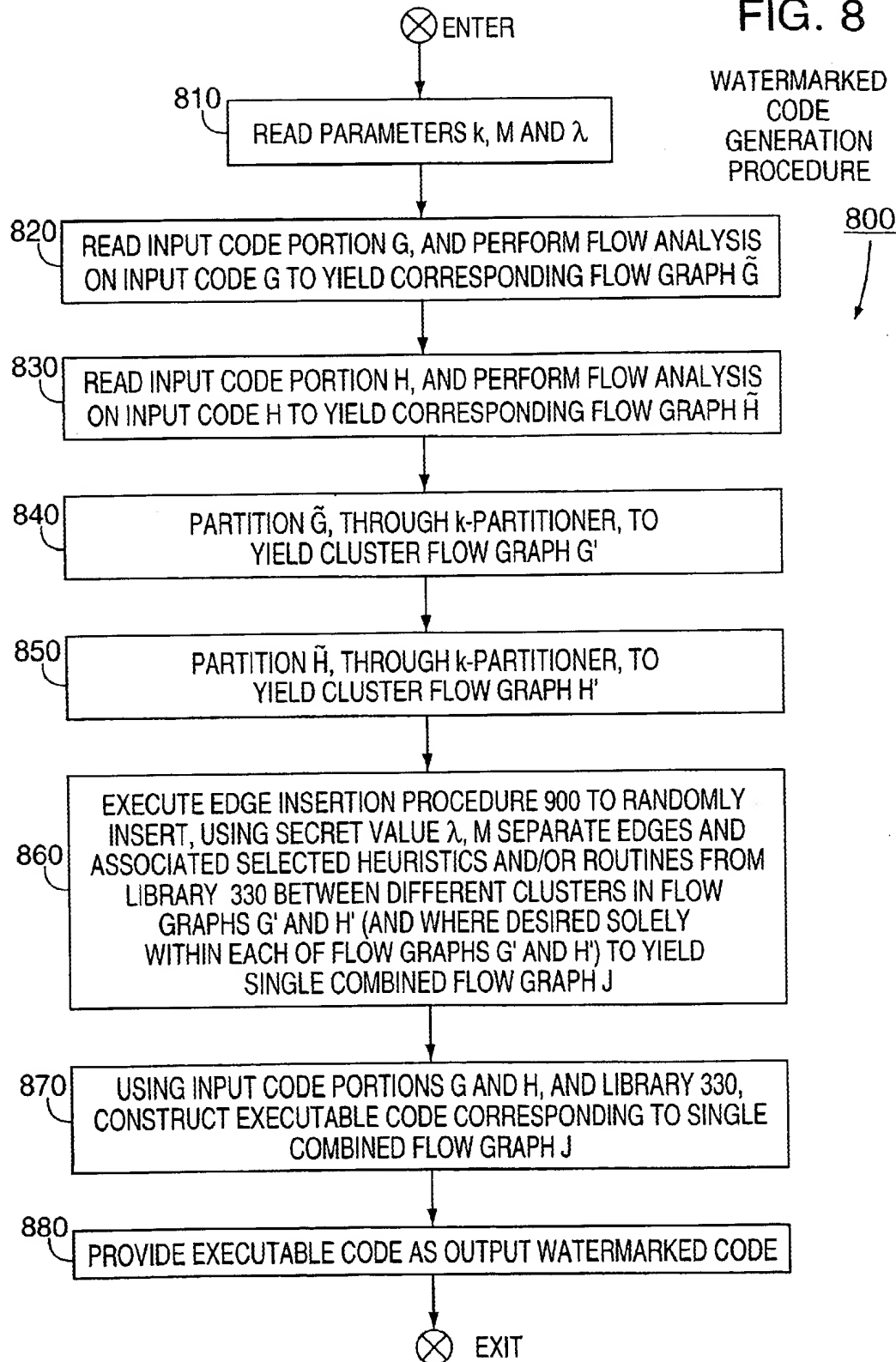


FIG. 8

WATERMARKED
CODE
GENERATION
PROCEDURE

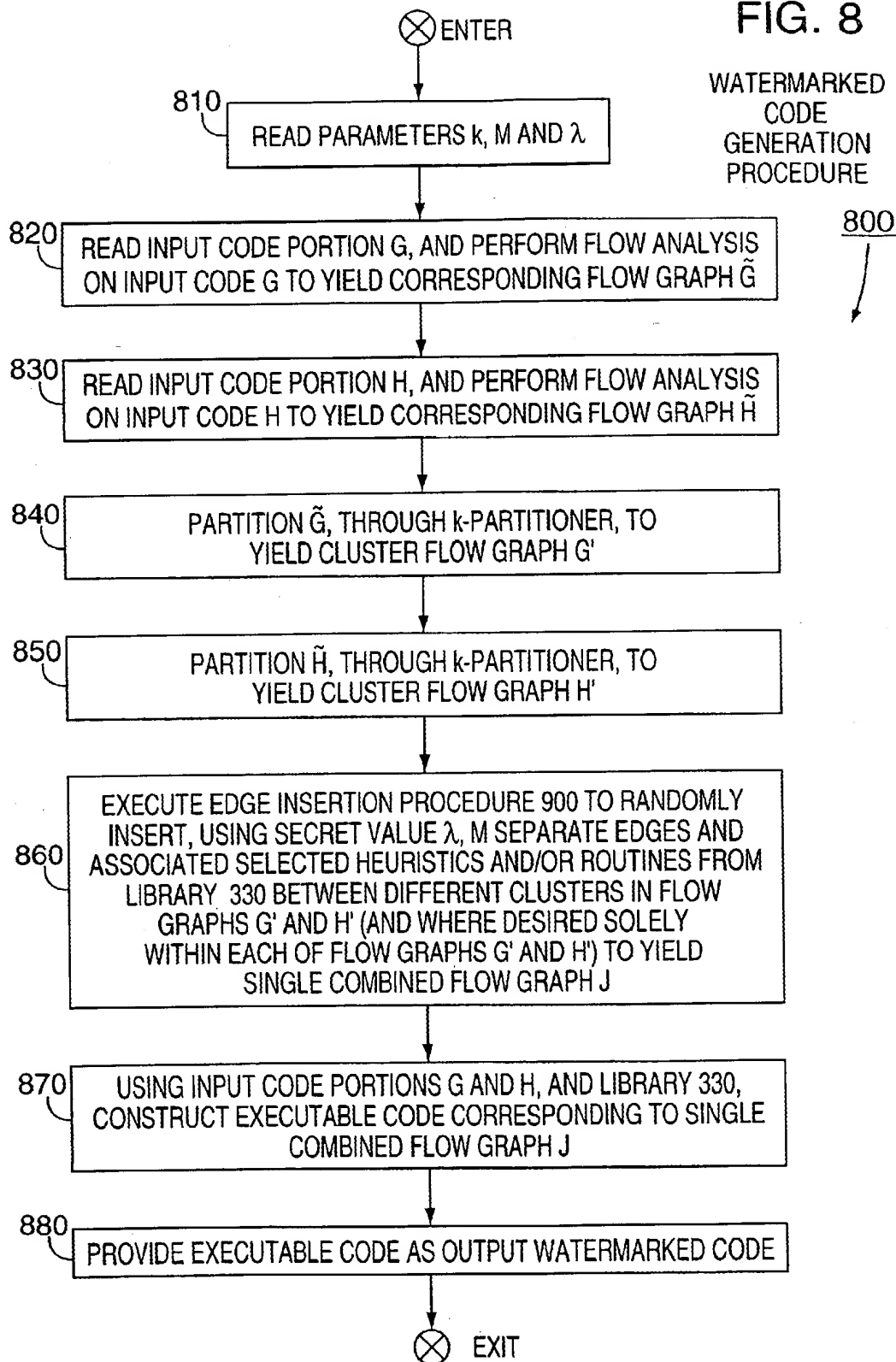


FIG. 9A

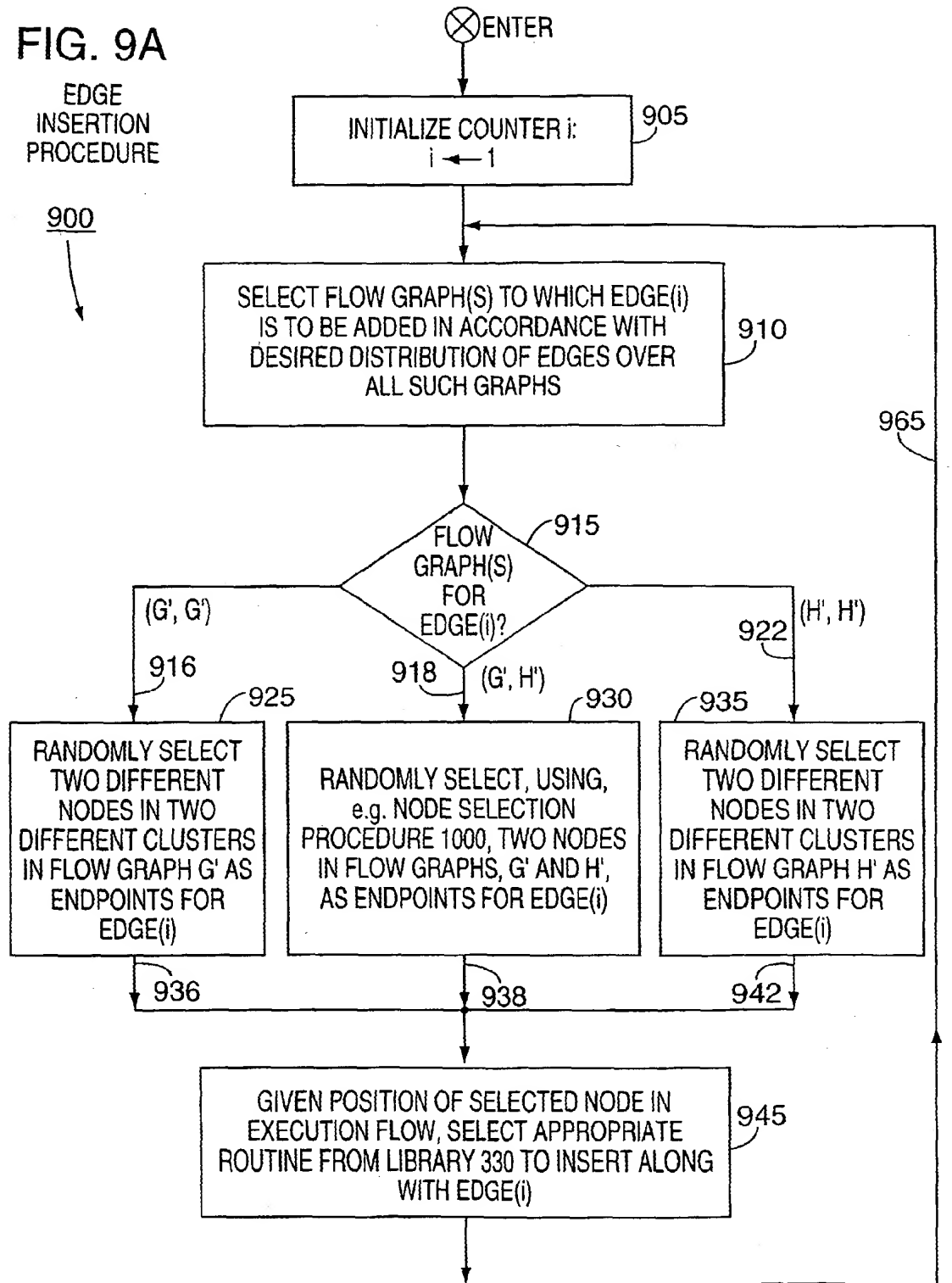
EDGE
INSERTION
PROCEDURE

FIG. 9B

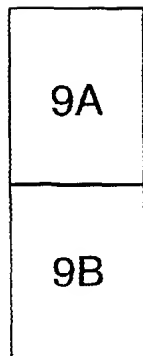
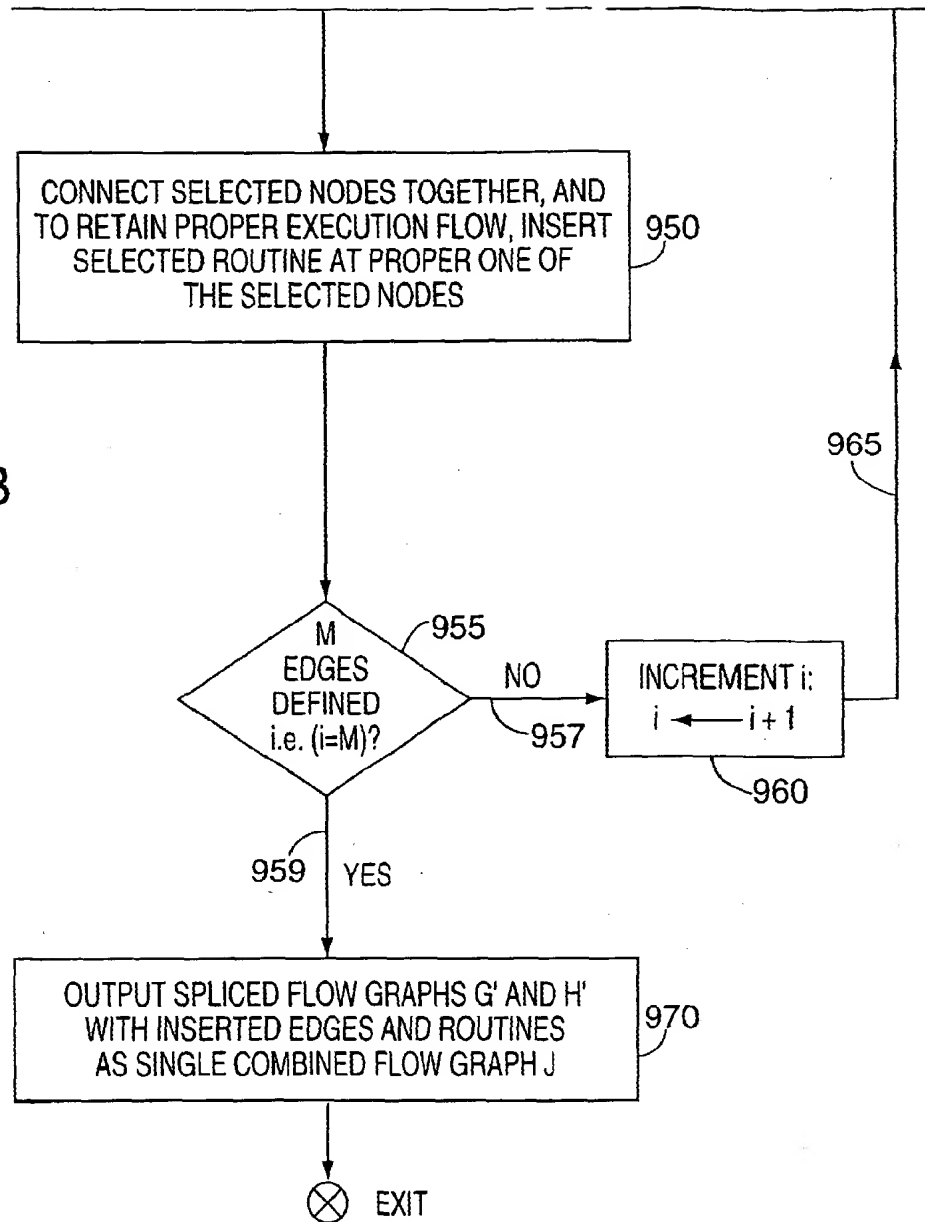


FIG. 9

FIG. 10

NODE
SELECTION
PROCEDURE